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FORM PTO – 1449	ATTORNEY DOCKET NO.: CBA-004
INFORMATION DISCLOSURE STATEMENT	APPLICANT(S): Vigoda et al.
	APPLICATION NO.: 10/686,009
	FILING DATE: October 15, 2003 GROUP: ~~2631~~ 2863

U.S. PATENT DOCUMENTS

EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
DW	A1	3,953,674	4/27/76	Fletcher et al.	178	69.5	4/4/75
	A2	4,667,327	5/19/87	Bright et al.	371	47	4/2/85
	A3	5,291,555	3/1/94	Cuomo et al.	380	6	12/14/92
	A4	5,293,425	3/8/94	Oppenheim et al.	381	71	12/3/91
	A5	5,309,199	5/3/94	Frick	355	77	2/5/93
	A6	5,394,405	2/28/95	Savir	371	27	4/24/92
	A7	5,412,665	5/2/95	Gruodis et al.	371	27	1/10/92
	A8	5,446,683	8/29/95	Mullen et al.	364	717	3/31/94
	A9	5,579,337	11/26/96	Grinstein et al.	375	206	6/19/95
	A10	5,612,973	3/18/97	Gershenfeld	375	206	6/19/95
	A11	5,729,388	3/17/98	Gershenfeld	359	637	9/19/96
	A12	5,737,360	4/7/98	Grinstein et al.	375	206	6/19/95
	A13	5,754,603	5/19/98	Thomas et al.	375	367	7/29/96
	A14	5,968,198	10/19/99	Hassan et al.	714	752	8/16/96
	A15	6,282,559	8/28/01	Helfenstein et al.	708	801	2/16/99
DW	A16	6,294,936	9/25/01	Clementi	327	156	9/28/98

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EXAM. INIT.		DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)
DW	B1	0 936 748 A2	8/18/66	EP	H04B	1/707	2/10/99	N	Y

OTHER ART, JOURNAL ARTICLES, ETC.

EXAM. INIT.	OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)	
DW	C1	Betancourt-Zamora et al., "1-GHz and 2.8-GHz CMOS Injection-locked Ring Oscillator Prescalers," Symposium on VLSI Circuits, June 14-16, 2001, Kyoto, Japan.

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DW	C2	Cuomo et al., "Circuit Implementation of Synchronized Chaos With Applications to Communications," The American Physical Society, Vol. 71, No. 1 (Jul. 1993).
	C3	Cuomo, "Synthesizing Self-Synchronizing Chaotic Systems," International Journal of Bifurcation and Chaos, Vol. 3, No. 5 (1993).
	C4	Cuomo et al., "Synchronization to Lorenz-Based Chaotic Circuits With Applications to Communications," IEEE Transactions on Circuits and Systems II, Vol. 40, No. 10 (Oct. 1993).
	C5	Edmonson et al., "Injection Locking Techniques for a 1-GHz Digital Receiver Using Acoustic-Wave Devices," IEEE, vol. 39 no. 5, pp. 631-637 (September 1992).
	C6	Guckenheimer et al., Applied Mathematical Sciences 42: Nonlinear Oscillations, Dynamical Systems, and Bifurcations of Vector Fields, Table of Contents, Chapter 1, Springer-Verlag New York Inc. (1983).
	C7	Kschischang, et al., "Factor Graphs and the Sum-Product Algorithm," IEEE, vol. 47 no. 2, pp. 498-519 (February 2001).
	C8	Lazzaro et al., "A Micropower Analog VLSI HMM State Decoder for Wordspotting," in Advances in Neural Information Processing System 9, M. C. Mozer, M. Jordan and T. Petsche, Eds., Cambridge, Mass: MIT Press (1997).
	C9	Loeliger et al., "Probability Propagation and Decoding in Analog VLSI," IEEE Trans. Information Theory, projected publication, September 9, 2000.
	C10	Lustenberger, "On the Design of Analog VLSI Iterative Decoders," Ph.D. Thesis, Swiss Federal Institute of Technology, Zurich (November 2000).
	C11	Simon et al., Spread Spectrum Communications Handbook, Table of Contents, Part 1, Chapter 5, Part 4, Chapters 1-3, McGraw-Hill Inc. (1994).
	C12	Vigoda, "A Nonlinear Dynamic System for Spread Spectrum Code Acquisition," Internet Article, 'Online!, August 9, 1999, pages 1-90.
	C13	Viterbi, "Principles of Spread Spectrum Communication," CDMA, Addison Wesley, pp. 39-67 (1995).
	C14	Wu et al., "A Simple Way to Synchronize Chaotic Systems With Applications to Secure Communication Systems," International Journal of Bifurcation and Chaos, Vol. 3, No. 6 (1993).
	C15	Yedidia et al., "Constructing Free Energy Approximations and Generalized Belief Propagation Algorithms," Mitsubishi Electrical Research Library, pp. 1-44 (April 2002).
DW	C16	PCT International Search Report for PCT/US00/31334 dated December 6, 2001

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